



Silicon Motion, Inc.

SM3261 Test Program and ISP Release Note

**SM3261 Test Program and ISP Release Note:**

Release Date	ISP Version	ISP Check Sum	Test AP Version	Description
2012/09/18	120911-AB	SM3261ABISP.BIN SM3261ABISP-27nm.BIN SM3261ABISP-24nm.BIN SM3261ABISP-B74.BIN SM3261ABISP-L84.BIN SM3261ABISP-SA27nmMLC.BIN SM3261ABISP-SA21nmMLC.BIN SM3261ABISP-ISPT24nm.BIN SM3261ABISP-IPSPD24nm.BIN SM3261ABISP-ISPSA21nm.BIN SM3261ABISP-ISPT19nm.BIN SM3261ABISP-IPSPD19nm.BIN SM3261ABISP-IPSPD32nm.BIN 0082BFAE	V 2.3.92 v3 12/09/13 Build	<ol style="list-style-type: none"> 1. First SM3261AB MP tool / FW formal release 2. Support Samsung 21nm TLC ABG-C die / ACG-A die: K9ABGD8U0C / K9ACGD8U0A (x1 mode) 3. Support Samsung 21nm MLC GCG-A die: K9GCGD8U0A (x1 mode) 4. Support SanDisk 19nm TLC 128Gb: SDTNQCAMA-016G (x1 mode) 5. Support Toshiba 19nm TLC 128Gb: TC58TEG7T2JTA00 (x1 mode) 6. Support Micron 25nm MLC L74 DDR / TLC B74 SDR: MT29F64G08CBAAB / MT29F64G08EBAAA (x1 mode) 7. Support Micron 20nm MLC L84 SDR/DDR: MT29F64G08CBABA / MT29F64G08CBABB (x1 mode)
2012/10/11	121002-AB	SM3261ABPTEST.BIN 0x38D8BB SM3261AAISP.BIN 008373AC	V 2.3.95 v9 12/10/09 Build	<ol style="list-style-type: none"> 1. Update LED Flash status. 2. Update Samsung 21nm TLC K9ACGD8U0Ax1/x2 performance. 3. Support SanDisk /Toshiba 19nm 64Gb TLC: SDTNQCAMA-008G / TC58TEG6T2JTA00 4. Support SanDisk 19nm TLC SDR to DDR mode
2012/10/24	121022-AB	SM3261ABISP.BIN	V 2.3.98 v1	<ol style="list-style-type: none"> 1. Support SanDisk 19nm TLC, SDTNQCBMA-032G (1CE



		00834E89 SM3261ABISP-ISPSPD19nm.BIN 00834E89	12/10/23 Build	2Die) 2. Support Samsung 21nm TLC, K9BDGD8U0A (1CE 2Die) 3. Support Micron L73 (4K page), MT29F32G08CBACA 4. Support Intel L85 SDR, 29F16B08LCMF3 5. Support Toshiba 19nm MLC type B (16K 1Plane), TC58TEG6DCJTA00
2012/12/06	121130-AB	SM3261ABISP SM3261ABISP-B74	V2.5.03 v3 12/11/29 Build	1. Modify remapping table location 2. Support Toshiba 19nm MLC type C (16K 2Plane), TC58TEG6DDJTA00 3. Support Micron B74 1CE/2Die: MT29F256G08EJAAA 4. Support Micron L85: MT29F128G08CBCAB 5. Support Toshiba 19nm TLC 32Gb: TC58TEG5T2JTA0 6. Disable cache program for Samsung 21nm MLC GCG-A die
2013/01/14	130108-AB	SM3261ABISP	V2.5.03 v3 12/11/29 Build	1. Revise PHY setting to improve compatibility 2. Suspend USB2/USB3 PHY in USB initialization.